12/2	U.S. UTILITY Patent Ar	pplication PATENT NAME PAT
	SCANNED ACC 4 Q.A. THE SCANNED APPLICATION NO. CONT/PRIOR CLASS SUBCLASS AF	RT UNIT EXAMINER H. VW. 7 TAGAS.
	Hiroki Shinkawata Semiconductor device and method of manu	ufacturing same
		PTO-2040 1299
		OSS REFERENCE(S) ASS (ONE SUBCLASS PER BLOCK)
	TERMINAL DRAWINGS Sheets Drwg: Figs. Drwg. Prir	CLAIMS ALLOWED Total Claims Print Claim for O.G.
	The term of this patent subsequent to (date) has been disclaimed (Assistant Examiner) The term of this patent shall not extend beyond the expiration date	NOTICE OF ALLOWANCE MAILED
	of U.S Patent. No.	Amount Due Date Paid Date Paid Date Paid Date Paid Date Paid

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